# AVS Mid-Atlantic Chapter DC Regional Meeting

Thursday, May 25, 2023 8:30 am - 1 pm Building 101, Green Auditorium, National Institute of Standards and Technology 100 Bureau Drive, Gaithersburg, MD

## FREE TO ALL, BUT PRE-REGISTRATION REQUIRED!

**Highlight theme: Semiconductor Packaging and Heterogeneous Integration** 

### **Invited speakers:**

- Christina DiMarino (Virginia Tech): "High-Density Packaging and Integration of Wide Bandgap Power Semiconductors"
- John Hryniewicz (Thorlabs Quantum Electronics): "Vertically integrated photonic device manufacturing at TQE"
- Kartik Srinivasan (NIST/UMD JQI): "Integrated Photonics for Clocks and Sensors"
- George Orji (NIST): "Research needs in Advanced Semiconductor Packaging".

#### **Equipment Exhibit - Vacuum hardware and instruments**

Poster Session - open to graduate and undergrad students working in AVS research

topics including:

Biomaterial Interfaces
Surface Science

Plasma Science

Plasma Science

**Electronic Materials & Processes Magnetic Interfaces & Nanostructures** 

MEMS & NEMS
Thin Films
Vacuum Technology

## NIST Lab Tours

Register here:



Cash Prizes for the best student posters!

Pre-registration required: http://goo.gl/forms/DD7y3fPUII

Registration Deadline: May 19, 2023

\*(foreign nationals are encouraged to apply early)

Questions?
Contact: Jacob Ricker
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Vendors contact: Doug Baker DBaker@Teledyne.com



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Learn about the advantages of membership in AVS!